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From-Thelen, Reid, & Priest LLP

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T-897 P.002/009 F-189

Docket No. 034299-000336

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT:

Jean-Luc Pelloie

APPLICATION NO.:

09/890,120

FILING DATE:

July 27, 2001

TITLE:

DYNAMIC THRESHOLD VOLTAGE MOS

TRANSISTOR FITTED WITH A CURRENT LIMITER, AND PROCESS FOR MAKING SUCH A TRANSISTOR

EXAMINER:

Edgardo Ortiz

ART UNIT:

2815

## CERTIFICATE OF MAILING

I hereby certify that this paper is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date printed below:

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## AMENDMENT

In response to the Office Action dated November 19, 2003, please amend the above-identified application as follows.

Amendments to the Claims are reflected in the listing of claims that begins on page 2 of this paper. Remarks/Arguments begin on page 6.

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